Notice of Allowability	Application No.	Applicant(s)	
	09/725,322 Examiner	DEO ET AL. Art Unit	
	Harold E. Dodds, Jr.	2167	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. X This communication is responsive to 11 March 2005 and 6 June 2005.			
2. X The allowed claim(s) is/are 3-6,8-19,21-27,29-32, 34-37, 39-42, and 48-53 (renumbered 1-41).			
3. ☑ The drawings filed on <u>23 December 2003</u> are accepted by the Examiner.			
4.			
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 5. ☐ Notice of Informal Patent Application (PTO-152) 6. ☑ Interview Summary (PTO-413), Paper No./Mail Date 061405. 7. ☑ Examiner's Amendment/Comment Statement of Reasons for Allowance of Biological Material			

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

- 2. Authorization for this examiner's amendment was given in a telephone interview with Lewis C. Lee on 19 May 2005. This authorization was confirmed in a second telephone interview on 14 June 2005.
 - 3. The application has been amended as follows:

For claim 3, on line 7 **delete** exposes **after** "system" and **insert** <u>uses</u> **after** "system".

For claim 22, on line 5 **delete** expose after "to", insert provide after "to", and insert access after "system".

For claim 23, on line 5 **delete** expose after "to", insert provide after "to", and insert access after "system".

For claim 41, on line 2 **delete** exposing **before** "a common set" and **insert** using **before** "a common set".

REASONS FOR ALLOWANCE

4. The following is an examiner's statement of reasons for allowance: The Examiner finds that the amendments to independent claims 3, 22, 23, and 48-50 filed on 11 March 2005 concerning an integrated circuit module place in condition for allowance independent claims 3, 22, 23, and 48-50. The combinations of prior art from

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Tobita et al. (U.S. Patent No. 6,421,279), Ginter et al. (U.S. Patent No. 6,427,140), and Chen (U.S. Patent No. 6,542,955) neither render obvious nor anticipates the combination of recited elements in light of claims 3, 22, 23, and 48-50. In particular the combination of art from Tobita, Ginter, and Chen does not render obvious the phrase "wherein individual functions defined in the set of application program interfaces include a parameter identifying whether an associated data file is stored in the volatile memory or the nonvolatile memory".

- 5. The Examiner finds that the amendments to independent claims 4 and 5 filed on 11 March 2005 concerning an integrated circuit module place in condition for allowance independent claims 4 and 5. The combinations of prior art from Tobita et al. (U.S. Patent No. 6,421,279) and Chen (U.S. Patent No. 6,542,955) neither render obvious nor anticipates the combination of recited elements in light of claims 4 and 5. In particular the combination of art from Tobita and Chen does not render obvious the phrase "wherein the file system comprises a memory region directory to identify whether the one or more data files are stored in the volatile memory".
- 6. The Examiner finds that the applicants' arguments on pages 41-49 of the "Preliminary Amendment" filed on 16 September 2004 concerning an integrated circuit module persuasive as applied to independent claims 10, 18, and 29. The combinations of prior art from Tobita et al. (U.S. Patent No. 6,421,279) and Chen (U.S. Patent No. 6,542,955) neither render obvious nor anticipates the combination of recited elements in light of claims 10, 18, and 29. In particular the combination of art from Tobita and Chen

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does not render obvious the phrase "a file location specifier to specify a physical location of the requested data file within the volatile memory or the nonvolatile memory identified by the memory region directory as containing the requested data file".

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- 7. The Examiner finds that the applicants' arguments on pages 41-49 of the "Preliminary Amendment" filed on 16 September 2004 concerning an integrated circuit module persuasive as applied to independent claims 25 and 39. The combinations of prior art from Tobita et al. (U.S. Patent No. 6,421,279), Li (U.S. Patent No. 6,519,594), and Chen (U.S. Patent No. 6,542,955) neither render obvious nor anticipates the combination of recited elements in light of claims 25 and 39. In particular the combination of art from Tobita, Li, and Chen does not render obvious the phrase "a memory region directory to identify whether a file is stored in the volatile memory or the nonvolatile memory".
- The Examiner finds that the amendments to independent claims 35 and 8. 36 filed on 11 March 2005 concerning storing data in a volatile data file in volatile memory of an integrated circuit module in condition for allowance independent claims 35 and 36. The combinations of prior art from Tobita et al. (U.S. Patent No. 6,421,279), Ginter et al. (U.S. Patent No. 6,427,140), Nobakht et al. (U.S. Patent No. 6,587,873) and Chen (U.S. Patent No. 6,542,955) neither render obvious nor anticipates the combination of recited elements in light of 35 and 36. In particular the combination of art from Tobita, Ginter, Nobakht, and Chen does not render obvious the phrase "ascertaining that the volatile data file is located in the volatile memory".

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CONCLUSION

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9. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Harold E. Dodds, Jr. whose telephone number is (571)-272-4110. The examiner can normally be reached on Monday - Friday 8:00 - 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John E. Breene can be reached on (571)-272-4107. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Harold E. Dodds, Jr.

Maroll E. Odds, R.

Patent Examiner

June 14, 2005